

Notice of Allowability

Application No.

10/717,081

Examiner

Paul W. Schlie

Applicant(s)

BUNGO, MOTOHIKO

Art Unit

2186

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to examiner initiated interview and resulting amended claims dated 6/19/06.
2. ☒ The allowed claim(s) is/are 1-3, 5-14.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☒ Examiner's Amendment/Comment
8. ☐ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

2. Authorization for this examiner's amendment was given in a telephone interview with Peter Ganjian on 6/19/06, whereby:

Claims 1, 5-6, 8 and 10 are amended; and

Claim 4 is cancelled; with otherwise remaining claims as previously presented.

3. The application has been amended as follows:

1. (Currently Amended) A memory module indicator device having [a] an indicator circuit using an indicator element to indicate situation of access to readable and writable semiconductor memory mounted on a standardized memory module connected to a computer,

[wherein] the indicator element is provided corresponding to a type of access to the semiconductor memory;

[wherein] the indicator circuit uses the indicator element corresponding to the access type to indicate frequency of the type of access to the semiconductor memory[.]; and

wherein the indicator circuit counts the number of accesses to the semiconductor memory for the access type during a specified period and uses the indicator element corresponding to the access type to provide an indication corresponding to the number of counted accesses.

5. (Currently amended) The memory module indicator device according to claim [4] 1, wherein there is provided a plurality of the indicator elements for each of

the access types; wherein the indicator circuit turns on indicator elements corresponding to the access type; and wherein the number of the indicator elements corresponds to the number of counted accesses.

6. (Currently amended) The memory module indicator device according to claim [4] 1, wherein the indicator circuit comprises a common counter circuit to count the number of accesses independently of the access types; wherein the indicator circuit receives a specified clock signal and generates a count signal whose state accordingly varies with the access type for each period corresponding to the specified period; and wherein, during the specified period, the indicator circuit uses the common counter circuit to count the number of accesses of the type corresponding to a state of the count signal and, after termination of the specified period, uses the indicator element corresponding to the access type to perform indication corresponding to the number of counted accesses.

8. (Currently amended) A memory module indicator device having a indicator circuit using an indicator element to indicate situation of access to readable and writable semiconductor memory mounted on a standardized memory module connected to a computer,

[wherein] the indicator circuit uses the indicator element to indicate a frequency of access to the semiconductor memory and holds an indication corresponding to the maximum frequency of the access[.]; and

wherein the indicator circuit counts the number of accesses to the semiconductor memory for the access type during a specified period and uses

the indicator element corresponding to the access type to provide an indication corresponding to the number of counted accesses.

10. (Currently amended) A memory module indicator device which uses an indicator element to indicate a situation of access to readable and writable semiconductor memory mounted on a standardized memory module having a memory module connection terminal connectable to a general-purpose motherboard connector provided on a computer, the device comprising: a connection terminal having the same shape as the memory module connection terminal;

a connector having the same shape as the motherboard connector; and an indicator circuit which uses the indicator element to perform indication corresponding to the frequency of access to the semiconductor memory when the connection terminal is connected to the motherboard connector and the memory module connection terminal is connected to the connector[.]; and wherein the indicator circuit counts the number of accesses to the semiconductor memory for the access type during a specified period and uses the indicator element corresponding to the access type to provide an indication corresponding to the number of counted accesses.


Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Paul W. Schlie whose telephone number is 571-272-6765. The examiner can normally be reached on Mon-Thu 8:00-6:00.

Art Unit: 2186

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Kim can be reached on 517-272-4182. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.


PIERRE BATAILLE
PRIMARY EXAMINER
6/20/06